

UNITED STATES PATENT AND TRADEMARK OFFICE  
**CERTIFICATE OF CORRECTION**

PATENT NO. : 7,023,065 B2  
APPLICATION NO. : 10/632176  
DATED : April 4, 2006  
INVENTOR(S) : Ayazi et al.

Page 1 of 1

It is certified that error appears in the above-identified patent and that said Letters Patent is hereby corrected as shown below:

*In column 10, line 2, change "602" to --650--.*

*In column 10, line 3, change "604" to --652--.*

*In column 10, line 7, add --by line 654-- after "shown".*

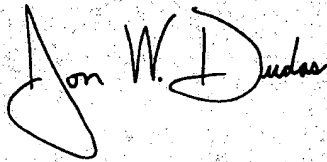
*In claim 14, line 13, after the word "coating" remove "the".*

*In claim 16, line 44, after the word "substrate" remove ";" and change to --,--.*

*In claim 28, line 3, after the word "the" remove "semiconductor-on-substrate" and change to --semiconductor-on-insulator substrate--.*

Signed and Sealed this

Thirteenth Day of March, 2007

A handwritten signature in black ink, reading "Jon W. Dudas". The signature is stylized, with a large, looped initial "J" and a distinct "D".

JON W. DUDAS  
Director of the United States Patent and Trademark Office